

### **Amendments to the Specification**

Please amend the first full paragraph on page 1 as follows:

The present invention relates to a label address translating device for obtaining an effective address (an absolute address, i.e., an intra-memory actual address) from a label in a program described in a predetermined language.

Please amend the third full paragraph on page 9 as follows:

Further, the program executing unit 16 executes a process of referring to the address during the execution of the program 20. For instance, the program executing unit 16 performs a certain arithmetic operation and stores an arithmetic result thereof in a predetermined address of the memory. At this time, the program executing unit 16, when recognizing that an invalid piece of data (invalid data) is described in the program 20, performs the exception handling (i.e., the label address translation process) other than the normal processes of the program 20.